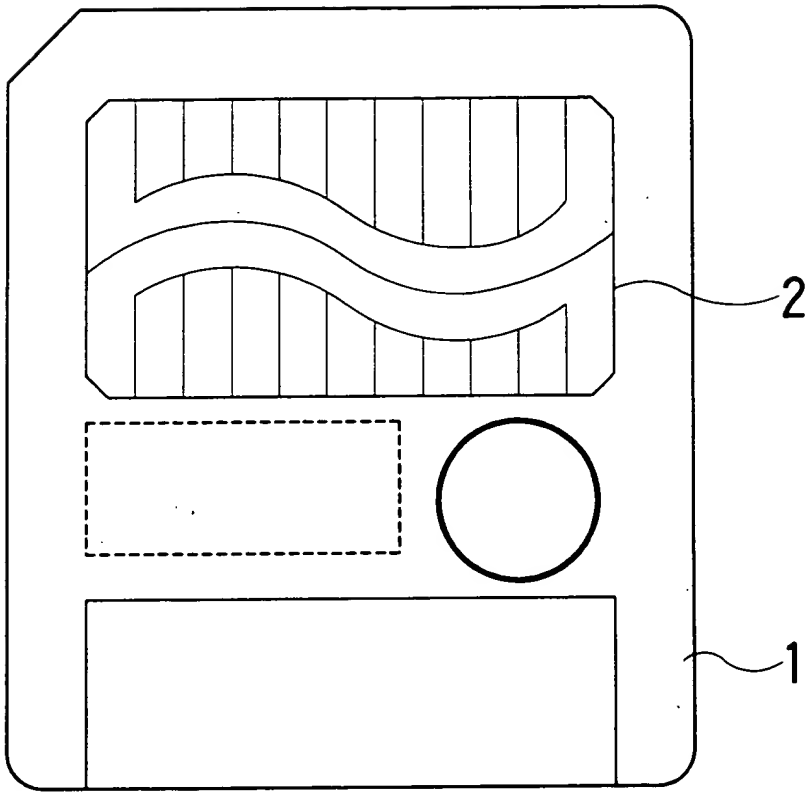


FIG. 1

PRIOR ART

		0 255	256 263
PHYSICAL BLOCK 0	Page 0	DATA AREA (256BYTES)	REDUNDANT DIVISION (16BYTES)
	Page 1		
	⋮		
	Page 15		
PHYSICAL BLOCK 1	Page 0		
	Page 1		
	⋮		
	Page 15		
⋮	⋮	⋮	⋮
PHYSICAL BLOCK 511	Page 0		
	Page 1		
	⋮		
	Page 15		

FIG.2

PRIOR ART

LOGICAL ~~PHYSICAL~~ BLOCK 0	SECTOR 0	512 BYTES
	SECTOR 1	
	⋮	
	SECTOR 7	
LOGICAL ~~PHYSICAL~~ BLOCK 1	SECTOR 8	
	SECTOR 9	
	⋮	
	SECTOR 15	
⋮	⋮	⋮
LOGICAL ~~PHYSICAL~~ BLOCK 499	SECTOR 3992	
	SECTOR 3993	
	⋮	
	SECTOR 3999	

FIG. 3

PRIOR ART

DATA DIVISION

BYTE	PAGE 0(EVEN PAGE)	PAGE 1(ODD PAGE)
0~255	DATA Area-1	DATA Area-2

REDUNDANT DIVISION

BYTE	EVEN PAGE	ODD PAGE
256	User Data Area	ECC Area-2
257		
258		
259		Block Address Area-2
260	Data Status Area	
261	Block Status Area	ECC Area-1
262	Block Address Area-1	
263		

FIG.4

PRIOR ART

		0 511	512 527
PHYSICAL BLOCK 0	Page 0	DATA AREA (256BYTES)	REDUNDANT DIVISION (16BYTES)
	Page 1		
	⋮		
	Page 15		
PHYSICAL BLOCK 1	Page 0		
	Page 1		
	⋮		
	Page 15		
⋮	⋮	⋮	⋮
PHYSICAL BLOCK 1023	Page 0		
	Page 1		
	⋮		
	Page 15		

FIG.5

PRIOR ART

LOGICAL BLOCK 0	SECTOR 0	512 BYTES
	SECTOR 1	
	⋮	
	SECTOR 15	
LOGICAL BLOCK 1	SECTOR 16	
	SECTOR 17	
	⋮	
	SECTOR 31	
⋮	⋮	⋮
LOGICAL BLOCK 999	SECTOR 15984	
	SECTOR 15985	
	⋮	
	SECTOR 15999	

FIG.6

PRIOR ART

DATA DIVISION

BYTE	
0~511	DATA Area

REDUNDANT DIVISION

BYTE	
512	User Data Area
513	
514	
515	
516	Data Status Area
517	Block Status Area
518	Block Address Area-1
519	
520	ECC Area-2
521	
522	
523	Block Address Area-2
524	
525	ECC Area-1
526	
527	

FIG.7

PRIOR ART

PHYSICAL ADDRESS	DATA DIVISION	REDUNDANT DIVISION (LOGICAL ADDRESS)
0	DATA	0
1	DATA	2
2	DATA	3
3	DATA	1
4	DATA	4
•	•	•
•	•	•
•	•	•
•	•	•

LOGICAL ADDRESS	LOGICAL ADDRESS
0	0
1	3
2	1
3	2
4	4
•	•
•	•
•	•
•	•

FIG.8

PRIOR ART

OFFSET (LOGICAL BLOCK ADDRESS)	PHYSICAL BLOCK ADDRESS	PHYSICAL BLOCK ADDRESS (BINARY DATA)			
		OPPER BYTE		LOWER BYTE	
word0(LBA=0)	0	0000	0000	0000	0000
word1(LBA=1)	500	0000	0001	1111	0100
word2(LBA=2)	327	0000	0001	0100	0111
⋮	⋮	⋮	⋮	⋮	⋮
word497(LBA=497)	244	0000	0000	1111	0100
word498(LBA=498)	249	0000	0001	1110	1111
word499(LBA=499)	128	0000	0001	1000	0000

FIG.9

PRIOR ART

OFFSET (LOGICAL BLOCK ADDRESS)	PHYSICAL BLOCK ADDRESS	PHYSICAL BLOCK ADDRESS (BINARY DATA)			
		OPPER BYTE		LOWER BYTE	
word0(LBA=0)	0	0000	0000	0000	0000
word1(LBA=1)	1000	0000	0011	1110	1000
word2(LBA=2)	654	0000	0010	1000	1110
⋮	⋮	⋮	⋮	⋮	⋮
word997(LBA=997)	488	0000	0001	1110	1000
word998(LBA=998)	498	0000	0001	1111	0010
word999(LBA=999)	256	0000	0001	0000	0000

FIG.10

PRIOR ART

D7	D6	D5	D4	D3	D2	D1	D0	256 + 8 BYTE/PAGE
0	0	0	1	BA10	BA9	BA8	BA7	262 BYTE(EVEN PAGE) 259 BYTE(ODD PAGE)
BA6	BA5	BA4	BA3	BA2	BA1	BA0	P	263 BYTE(EVEN PAGE) 260 BYTE(ODD PAGE)

BA10~BA0:LOGICAL BLOCK ADDRESS

P EVEN PARITY BIT "1" FIXED VALUE

FIG. 11

PRIOR ART

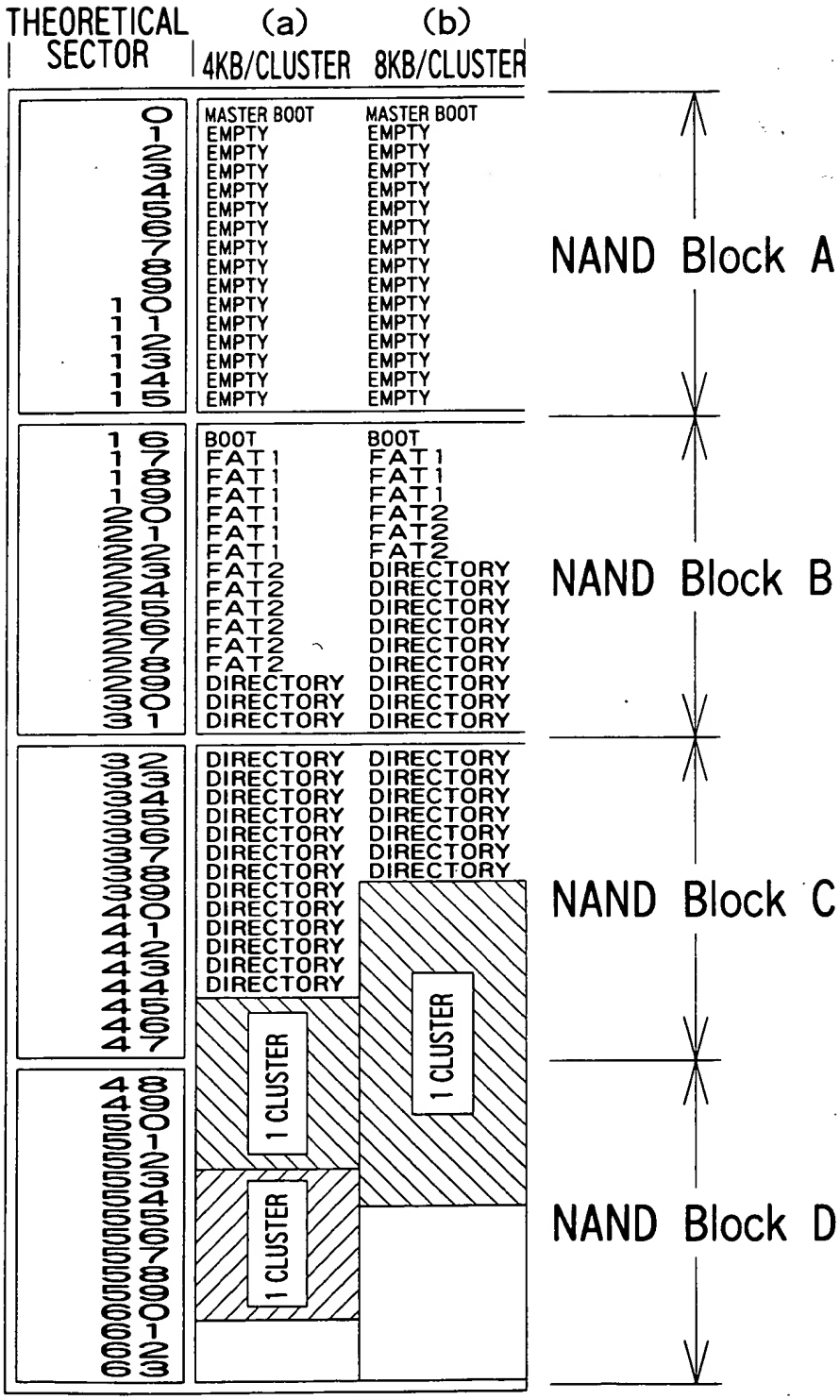


FIG. 12

PRIOR ART

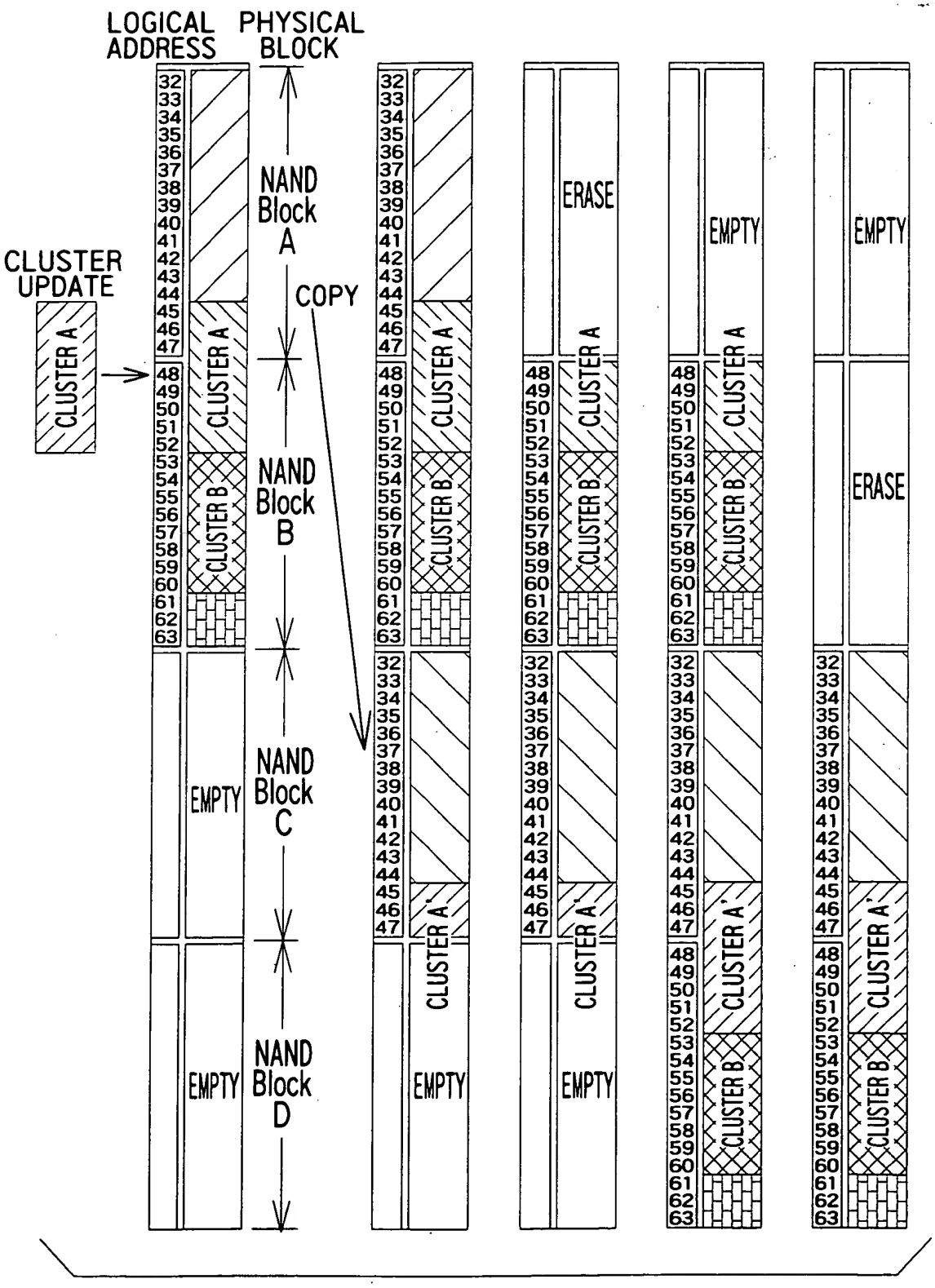


FIG. 13
PRIOR ART

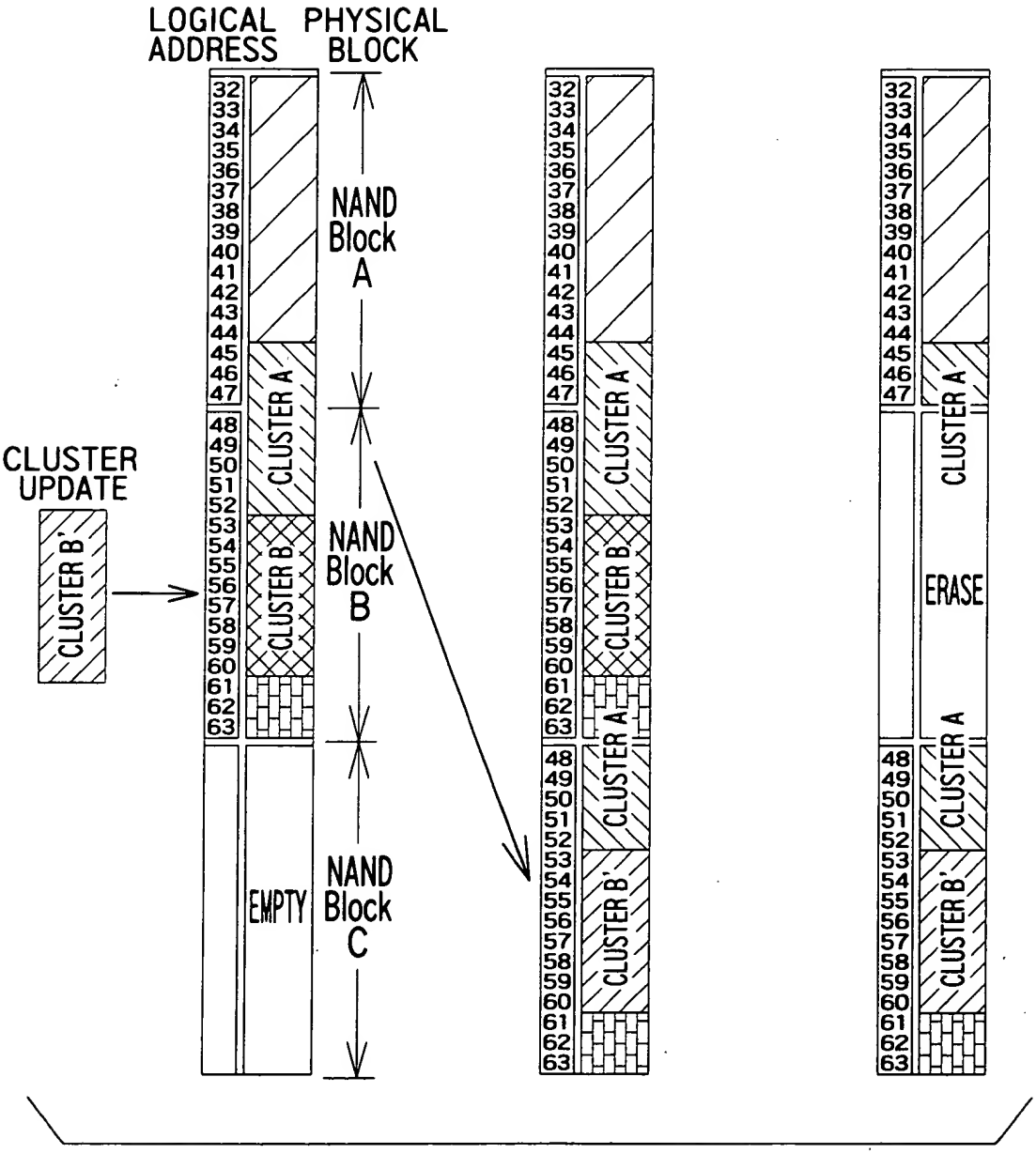


FIG. 14

PRIOR ART

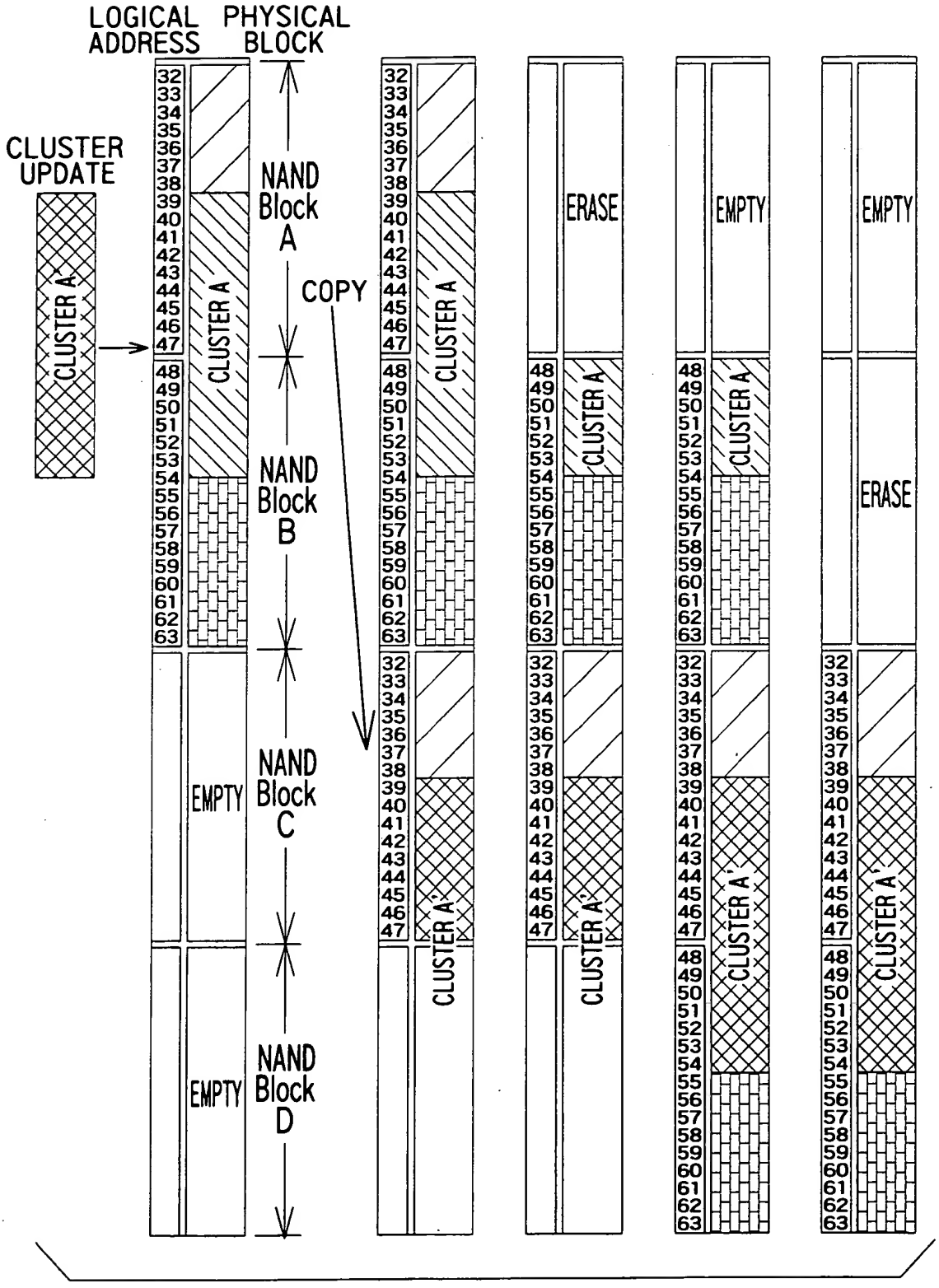


FIG. 15

PRIOR ART

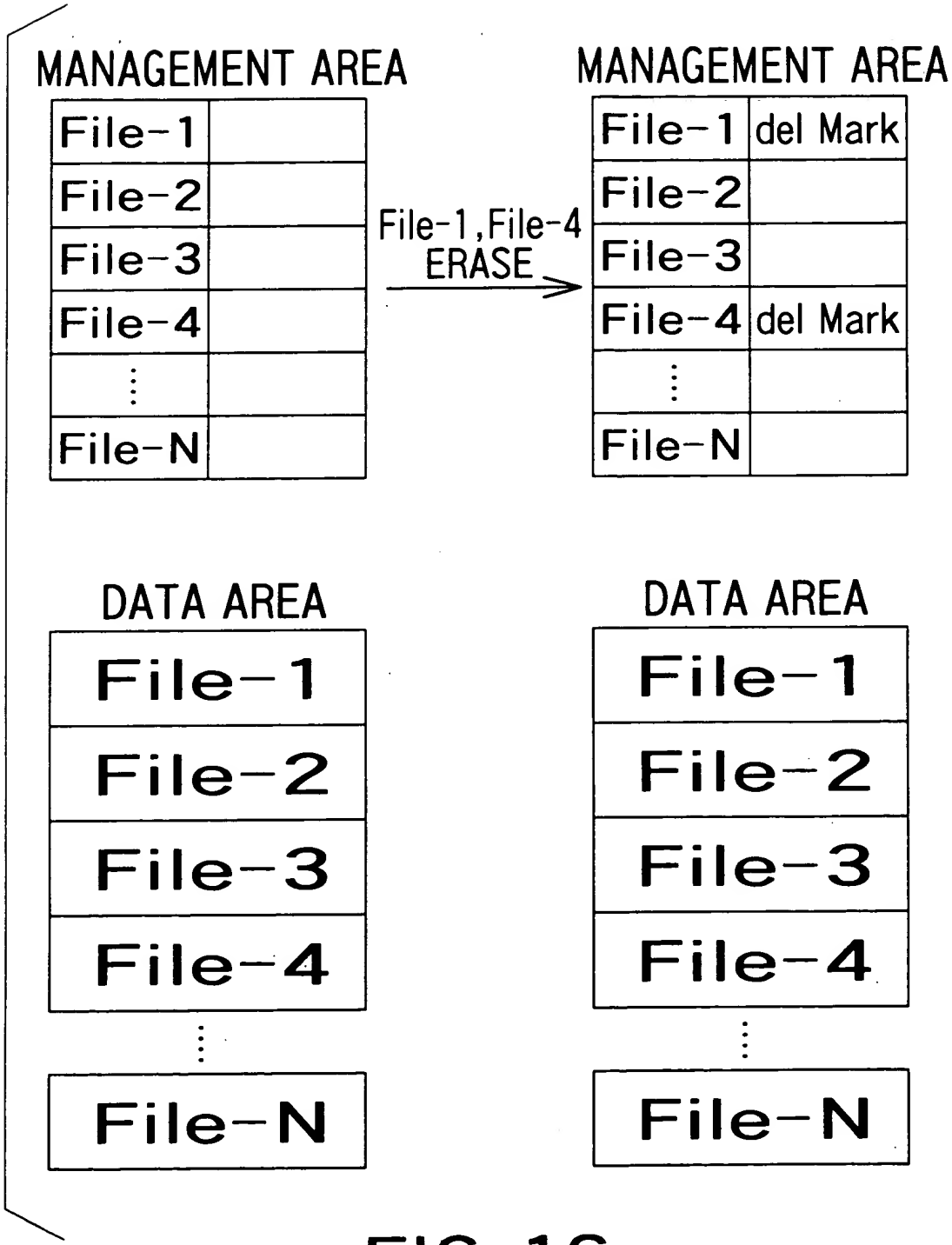


FIG.16

PRIOR ART

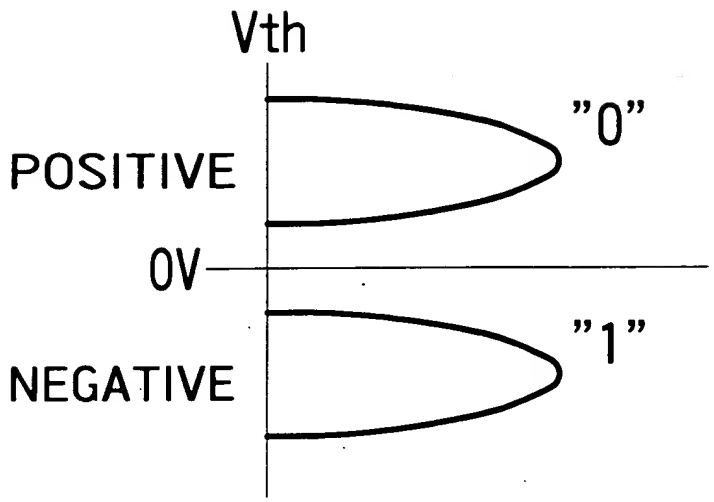


FIG. 17

PRIOR ART

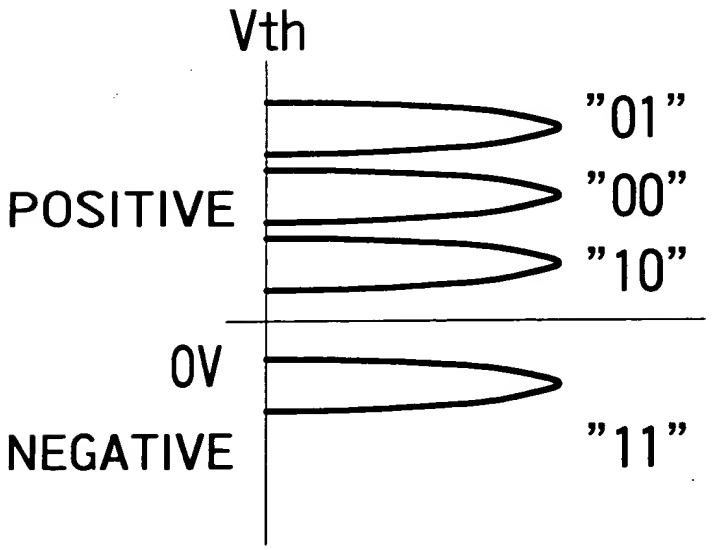


FIG. 18

PRIOR ART

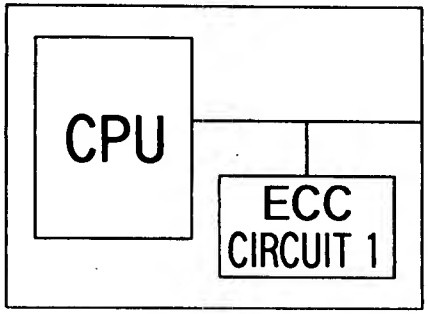
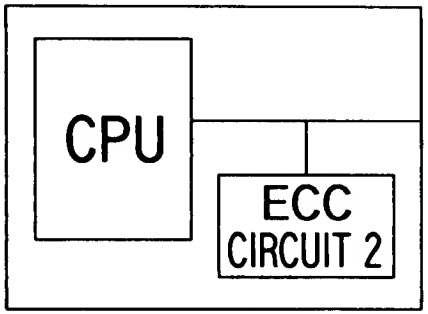
	CARD IN FIG.2(a)	CARD IN FIG.2(b)
 SYSTEM A	AVAILABLE	VNAVAILABLE
 SYSTEM B	VNAVAILABLE	AVAILABLE

FIG.19

PRIOR ART